ABSTRACT OF THE DISCLOSURE

A common bias section is composed of a first series circuit having an internal resistor R1 and an external resistor Rext connected in series and an operational amplifier OP1 having a first input terminal connected to a reference voltage Vref, a second input terminal connected to a node Vr1, and an output terminal connected to the series circuit. An impedance trimming section is composed of a series circuit having an internal resistor Rto and an impedance dummy resistor Rto_trim connected in series, a comparator CMP having a first input terminal connected to the node Vr1 and a second input terminal connected to a node Vto1, a code control circuit which uses a clock signal CLK to latch an output signal from the comparator CMP to generate a plurality of switching codes, and a switching circuit which switch a resistance value of the impedance dummy resistor Rto_trim.